Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (previously presented):

A method, comprising:

receiving from a first requester a request to write a second indicator that comprises an identifier of the first requester to a first indicator that indicates if a component is available;

determining, in response to receiving the write request from the first requester, whether the component is available; and

replacing, if the component is available, the first indicator indicating that the shared resource is available, with the second indicator reducing access to the component, to permit access to the component by the first requester.

2-6, (cancelled)

(previously presented):

The method of claim 1, wherein determining, in response receiving the write request from the first requester, whether the component is available comprises determining a presence of the first indicator.

8. (currently amended):

The method of claim 1, wherein determining, in response to receiving the write request from the first requester, whether the component is available comprises determining based on an external indicator that the component is available.

9. (original):

The method of claim 8, wherein the external indicator comprises a flag.

10. (previously presented):

The method of claim 1, further comprising receiving, by execution of a write request by a second requester upon completion of access to the component by the second requester, a third indicator increasing access to the component to replace the second indicator.

11. (original):

The method of claim 10, wherein the operation comprises a write of the third indicator.

12. (original):

The method of claim 11, wherein the first requester and the second requester comprise processes.

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13. (original):

The method of claim 11, wherein the first requester and the second requester comprise one requester.

14. (original):

The method of claim 11, wherein the first indicator and the third indicator comprise a same indicator.

15. (previously presented):

A method, comprising:

receiving from a first requester a request to write a second indicator that comprises an identifier of the first requester to a first indicator that indicates if a component is available;

determining, in response to receiving the write request from the first requester, whether the component is available;

prohibiting accesses to the component by one or more additional requesters;

replacing, because the component is available, the first indicator indicating that the component is available, with the second indicator reducing access to the component; and allowing the first requester to indicate that the component is available.

16-18. (cancelled)

19. (previously presented):

The method of claim 15, wherein the first indicator is maintained in a set-by-write register written to by processes attempting to access the component.

20-21. (cancelled)

22. (previously presented):

The method of claim 15, wherein determining, in response to receiving the write request from the first requester, whether the component is available comprises determining a presence of the first indicator indicating that a component is available.

23. (previously presented):

The method of claim 15, wherein determining, in response to receiving the write request from the first requester, whether the component is available comprises determining based on an external indicator that the component is available.

24. (original):

The method of claim 23, wherein the external indicator comprises a flag.

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25. (currently amended):

The method of claim 15, 17, further comprising:

receiving from a second requester a request to write a third indicator increasing access to the component;

determining, because of receiving the third indicator, that access to the component has been increased; and

replacing the second indicator with the third indicator increasing access to the component.

26. (original):

The method of claim 25, wherein the first requester and the second requester comprise one requester.

27. (currently amended):

A method of obtaining access to a shared resource, comprising:

receiving a request to write writing a second value to a register; and

changing a first value to the second value in response to the request to write changing, as a result of writing to the register if the register detects a that the first value indicates value; indicating that the shared resource is available; and available, the first value to the second value; and

limiting access to the shared resource.

28-29. (cancelled)

(currently amended):

The method of claim 27, 29, wherein the second value comprises an identifier of a process that requests the writing of the second value.

31. (original):

The method of claim 30, further comprising reading the register to determine that the register contains the identifier.

32. (currently amended):

The method of claim 27, wherein the shared resource comprises a peripheral device in a computer system. system and the peripheral device contains the register.

33. (original):

The method of claim 27, further comprising:

receiving from the register the first value indicating that the shared resource is available; accessing the shared resource; and

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> changing upon completion of access to the shared resource, the second value to a third value increasing access to the shared resource.

34. (original):

The method of claim 33, wherein changing the second value to the third value comprises writing the third value to the register.

35. (original):

The method of claim 34, wherein the first value, the second value and the third value comprise variables.

36. (original):

The method of claim 35, wherein the first value and the third value comprise a same variable.

37. (previously presented):

A method, comprising:

executing by a process a request from a requester to write an identifier of the requester; determining by a register, in response to the request, whether a first indicator allowing access to a shared resource is present in the register;

changing by the register if the first indicator is present, the first indicator to a second indicator comprising the identifier of the requester and reducing access the shared resource:

sending by the register the first indicator to the process;

determining by the process receipt of the first indicator;

using by the process the shared resource; and

replacing by the process the second indicator with a third indicator increasing access to the shared resource.

38-40. (cancelled)

41. (previously presented):

The method of claim 37, wherein the register comprises a set-by-write register.

42. (cancelled)

43. (previously presented):

The method of claim 37, further comprising reading the register to determine that the register contains the identifier.

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44. (original):

The method of claim 43, wherein replacing the second indicator with the third indicator comprises writing the third indicator to the register.

45. (original):

The method of claim 44, wherein the first indicator and the third indicator comprise the same indicator.

46. (previously presented):

An article of manufacture comprising a machine-accessible medium including thereon sequences of instructions that, when executed, cause a machine to:

receive from a first requester a request to write a second indicator that comprises an identifier of the first requester to a first indicator that indicates if a component is available;

determine, in response to the write request received from the first requester, whether a component is available; and

replace, if the component is available, the first indicator indicating that the shared resource is available, with the second indicator reducing access to the component, to permit access to the component by the first requester.

47-51. (cancelled)

52. (previously presented):

The article of claim 46, wherein the sequences of instructions that cause the machine to determine, in response to the write request received from the first requester, whether the component is available comprise sequences of instructions that, when executed, cause the machine to determine, in response to the request to write the second indicator by the first requester, a presence of the first indicator.

53. (previously presented):

The article of claim 46, wherein the sequences of instructions that cause the machine to determine, in response to the write request received from the first requester, whether the component is available comprise sequences of instructions that, when executed, cause the machine to determine, in response to the request to write the second indicator by the first requester, that the component is available based on an external indicator.

54. (previously presented):

The article of claim 53, wherein the sequences of instructions that cause the machine to determine, in response to the write request received from the first requester, based on an external indicator that the component is available comprise sequences of instructions that, when executed, cause the machine to determine, in response to receiving the write request from the first requester, that the component is available based on a flag.

55. (previously presented):

The article of claim 46, further comprising sequences of instructions that, when executed, cause the machine to receive, by execution of an operation by a second requester upon completion of access to the component by the second requester, a third indicator increasing access to the component to replace the second indicator.

56. (original):

The article of claim 55, wherein the sequences of instructions that cause the machine to receive, by execution of the operation by the second requester upon completion of access to the component by the second requester, the third indicator increasing access to the component to replace the second indicator comprise sequences of instructions that, when executed, cause the machine to receive, by execution of a write of the third indicator by the second requester upon completion of access to the component by the second requester, the third indicator increasing access to the component to replace the second indicator.

57. (original):

The article of claim 56, wherein the first requester and the second requester comprise processes.

58. (original):

The article of claim 56, wherein the first requester and the second requester comprise one requester.

59. (original):

The article of claim 56, wherein the first indicator and the third indicator comprise a same indicator.

60. (previously presented):

An article of manufacture comprising a machine-accessible medium including thereon sequences of instructions that, when executed, cause a machine to:

receive from a first requester a request to write a second indicator that comprises an identifier of the first requester to a first indicator that indicates if a component is available;

determine, in response to receiving the write request from the first requester, whether the component is available;

prohibit accesses by one or more additional requesters;

replace, because the component is available, the first indicator indicating that the component is available, with the second indicator reducing access to the component;

and allow the first requester to indicate that the component is available.

61-63. (cancelled)

64. (previously presented):

The article of claim 60, wherein the first indicator and the second indicator are maintained in a set-by-write register written to by processes attempting to access the component.

65. (cancelled)

66. (previously presented):

The article of claim 60, wherein the sequences of instructions that cause the machine to prohibit accesses by one or more additional requesters comprise sequences of instructions that, when executed, cause the machine to prohibit writes of identifiers of processes attempting to access the component by one or more additional requesters.

67. (previously presented):

The article of claim 60, wherein the sequences of instructions that cause the machine to determine, in response to receiving the write request from the first requester, whether the component is available comprise sequences of instructions that, when executed, cause the machine to determine, in response to receiving the write request from the first requester, a presence of the first indicator.

68. (previously presented):

The article of claim 60, wherein the sequences of instructions that cause the machine to determine, in response to receiving the write request from the first requester, whether the component is available comprise sequences of instructions that, when executed, cause the machine to determine, in response to receiving the write request from the first requester, that the component is available based on an external indicator.

69. (previously presented):

The article of claim 68, wherein the sequences of instructions that cause the machine to determine, in response to receiving the write request from the first requester, based on an external indicator that the component is available comprise sequences of instructions that, when executed, cause the machine to determine, in response to receiving the write request from the first requester, that the component is available based on a flag.

70. (previously presented):

The article of claim 60, further comprising sequences of instructions that, when executed, cause a machine to:

receive from a second requester a third indicator increasing access to the component; determine, because of receiving third indicator, that access to the component has been increased; and

replace the second indicator with the third indicator increasing access to the component.

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71. (original):

The article of claim 70, wherein the first requester and the second requester comprise one requester.

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72. (currently amended):

An apparatus comprising:

- a resource:
- a storage area in the resource;
- a first value in the storage area, which the storage area changes to a second value in response to a request from a requester to write an identifier of the requester that is included in the request to the storage area; and

the second value in the storage area.

73. (original):

The apparatus of claim 72, wherein the resource comprises a peripheral device in a computer system.

74. (original):

The apparatus of claim 72, wherein the storage area comprises a register.

75-76. (cancelled)

77. (original):

The apparatus of claim 72, wherein the first value indicates that the resource is available.

78. (original):

The apparatus of claim 72, wherein the second value reduces access to the resource.

79, (original):

The apparatus of claim 72, wherein the storage area is linked to other storage areas containing the first value.

80. (original):

The apparatus of claim 79, wherein the other storage areas change the first value to the second value when the storage area changes the first value to the second value.